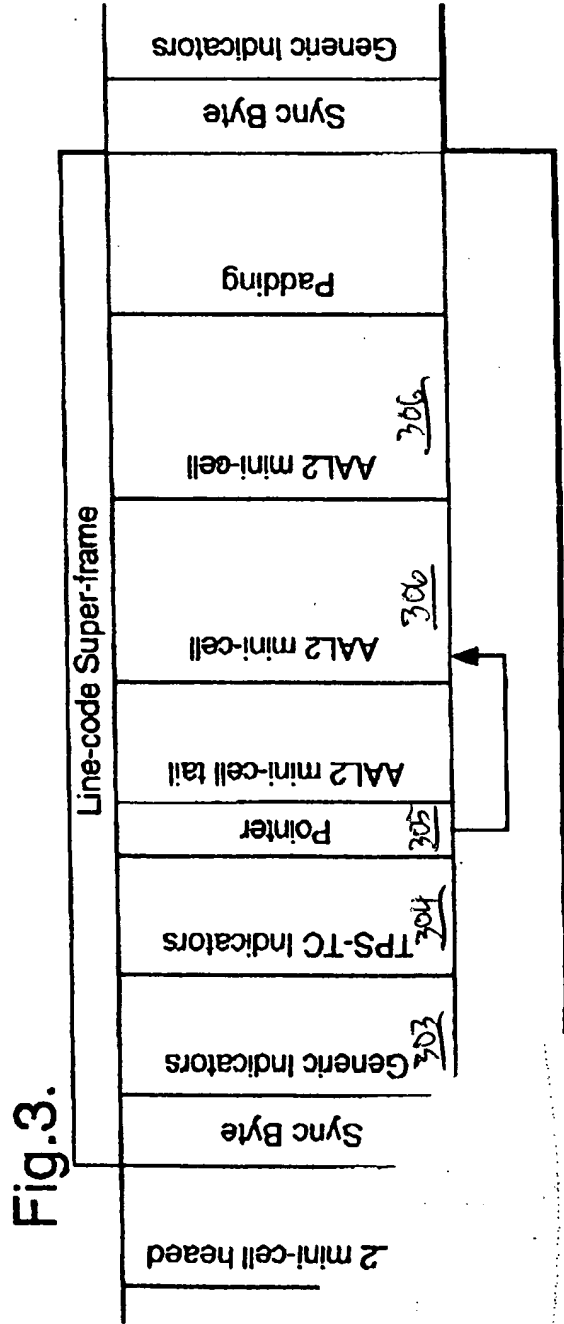
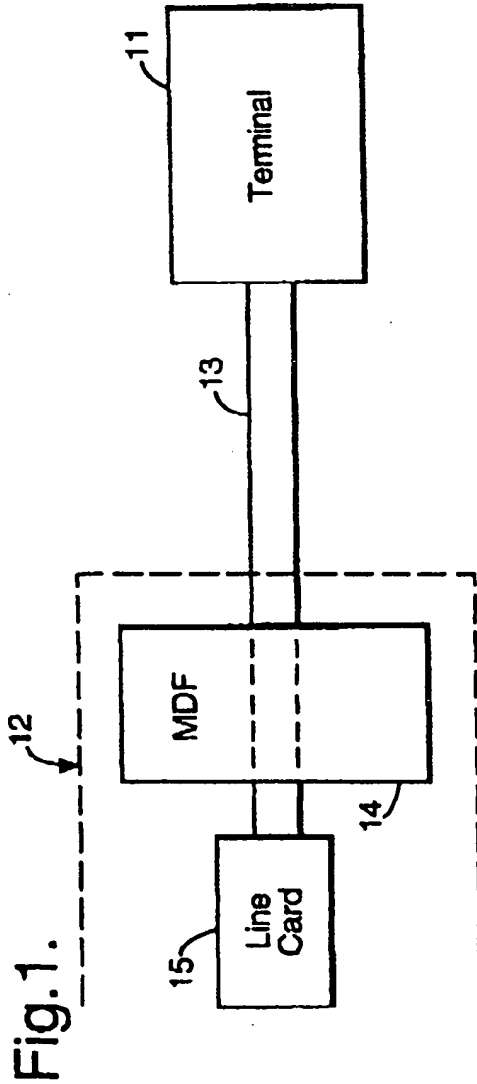


OK-Fig1-8  
 22  
 12 DEC 02



BEST AVAILABLE COPY

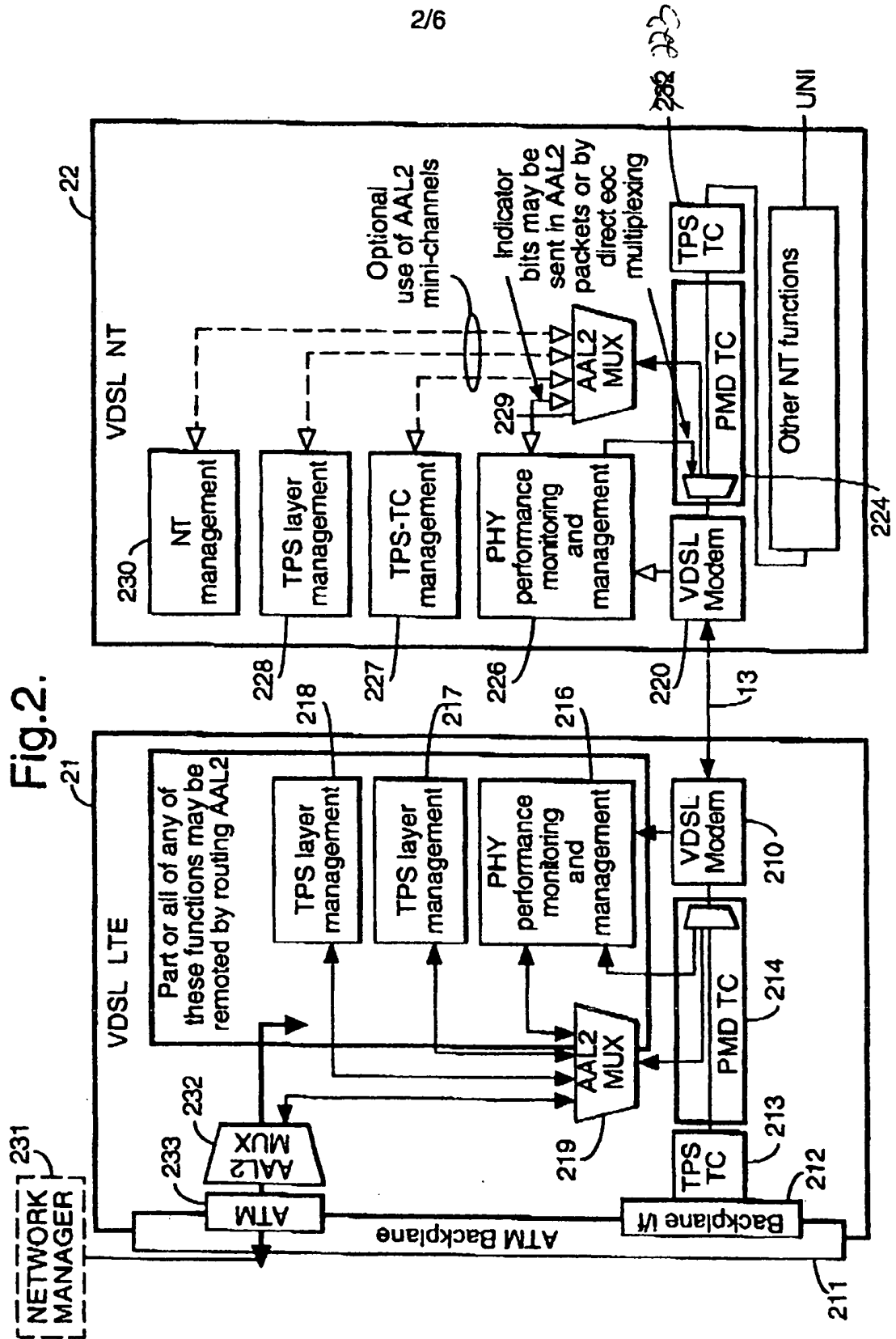
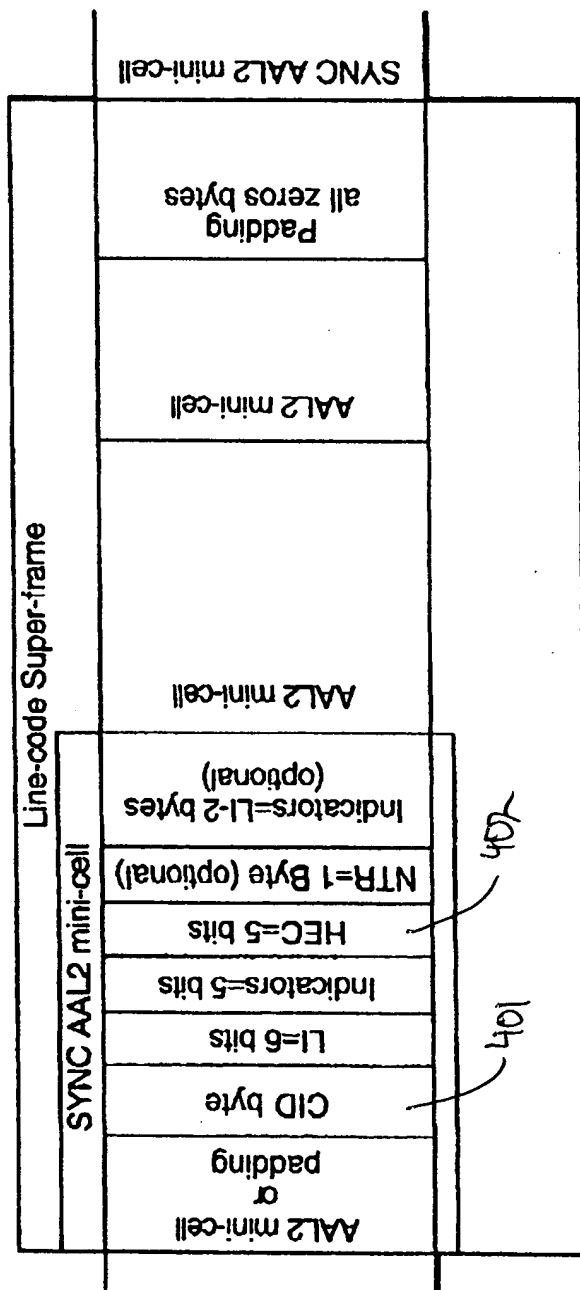


Fig.4.



BEST AVAILABLE COPY

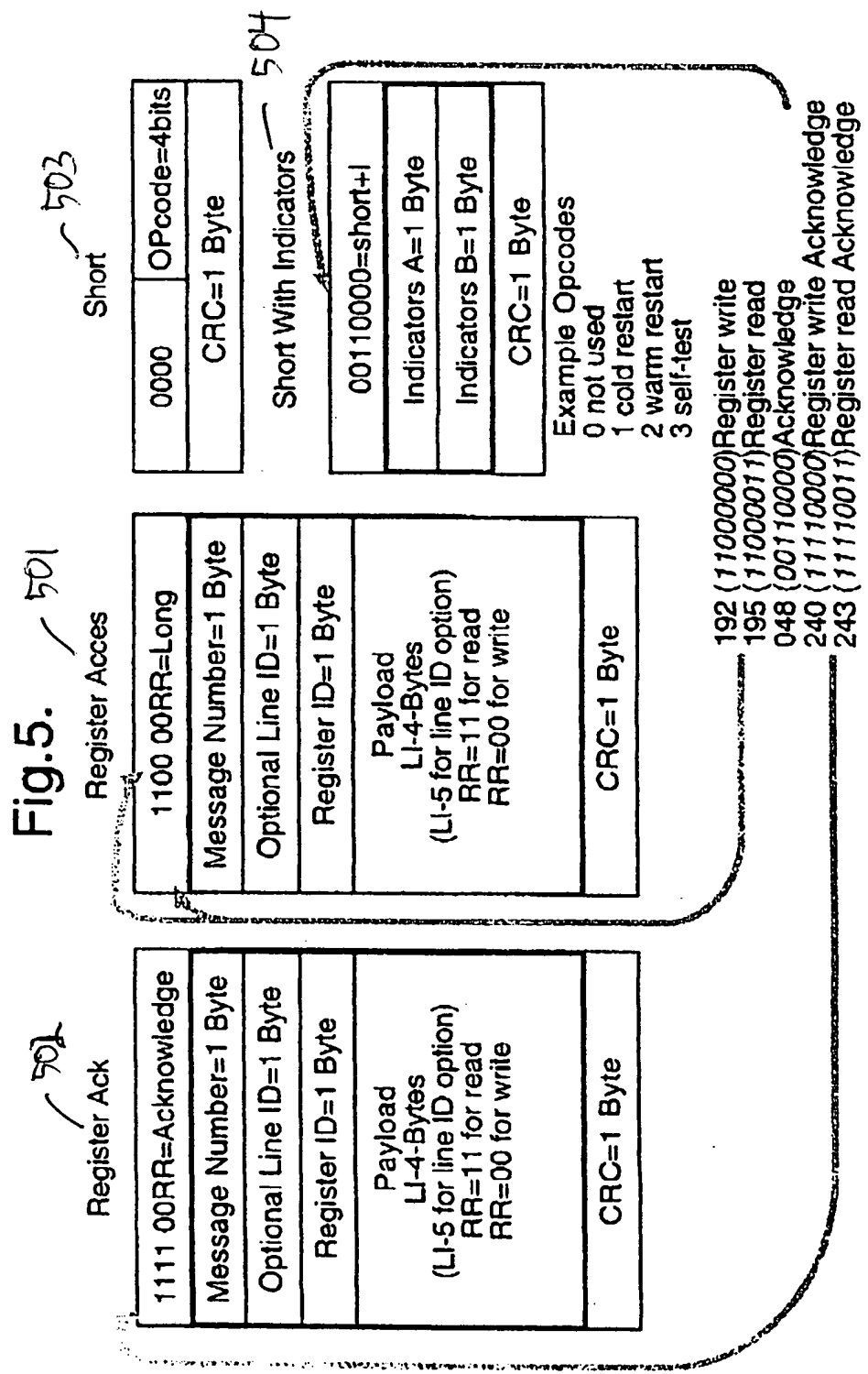
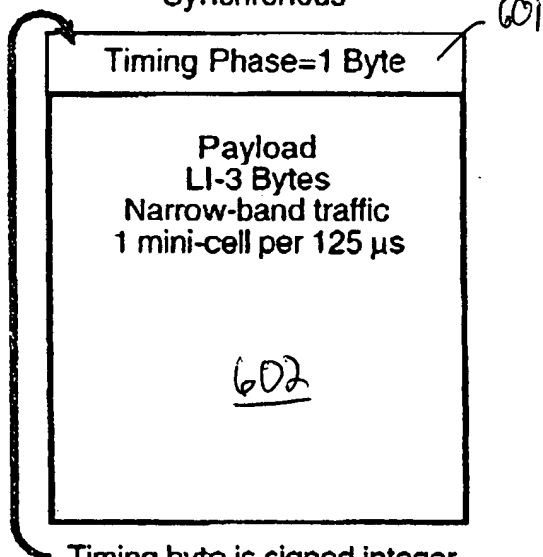


Fig.6a.

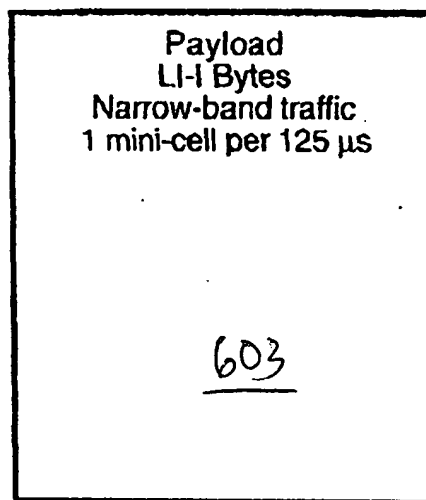
Synchronous



Timing byte is signed integer  
giving offset from NTR phase

Fig.6b.

Pleisochronous



BEST AVAILABLE COPY

